



UNIVERSITI TUN HUSSEIN ONN MALAYSIA

FINAL EXAMINATION SEMESTER II SESSION 2010/2011

COURSE	:	ELECTRICAL CIRCUIT THEORY
COURSE CODE	:	BEE 1113 / BEX 10103
PROGRAMME	:	BEE
EXAMINATION DATE	:	APRIL / MAY 2011
DURATION	:	3 HOURS
INSTRUCTION	•	ANSWER FIVE (5) QUESTIONS ONLY.

THIS PAPER CONSISTS OF NINE (9) PAGES

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BEE1113 / BEX10103

- Q1 (a) An electric current is the rate of change of charge and measured in amperes (A).
 - (i) With the help of diagram, describe how does current exist and flow in conductor.

(4 marks)

(ii) If the current flowing through an element is given by:

$$i(t) = \begin{cases} 3t \ A, \ 0 < t < 6 \ s \\ 18 \ A, \ 6 < t < 10 \ s \\ -12 \ A, \ 10 < t < 15 \ s \\ 0 \ A, \ t > 15 \ s \end{cases}$$

Plot the charge stored in the element over $0 < t < 20 \ \mu s$.

(5 marks)

- (b) A close look at the circuit in Figure Q1(b) reveal that there are two closed paths, the one on the left with the current i_s and the one on the right with the current i_0 .
 - (i) Use Kirchhoff's laws and Ohm's law to find the voltage Vo. (4 marks)
 - (ii) Show that your solution is consistent with the constraint that the total power developed in the circuit equals the total power dissipated.

(7 marks)

Q2	(a)	Given the circuit in Figure Q2(a), find			
		(i)	the values of i_a , i_b and V_o	(4 marks)	
		(ii)	the power dissipated in each resistor.	(5½ marks)	
		(iii)	the power delivered by the 50 V source.	(1½ marks)	
(b)	For the circuit in Figure Q2(b) , find value of R and the power supplied by the 250 V source.				
			(9 marks)		

Q3 (a) Use source transformations to find the voltage, V_o in the circuit shown in Figure Q3(a). Hence, determine the power developed by the 250 V voltage source and the power developed by the 8 A current source.

(10 marks)

BEE1113 / BEX10103

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(b) Figure Q3(b) shows a network with three independent sources and the current, i flows through 3 Ω resistor. Apply the superposition theorem to this network in order to determine the value of i.

(10 marks)

(8 marks)

Q4	(a)	For the circuit shown in Figure Q4(a),				
(t		(i)	find the value of R that result in maximum power being transferred to the terminals a-b.			
			(4	4 marks)		
		(ii)	calculate the maximum power that can be delivered to R. (7	7 marks)		
	(b)	Obtain and draw the Thevenin and Norton equivalent circuits at terminals a-b for t		b for the		
		circuit	iown in Figure Q4(b). (9 mar	9 marks)		
Q5 (a)	(a)	The initial values of i_1 and i_2 in the circuit shown in Figure Q5(a) are +3 and -5 A, respectively. The voltage at the terminals of the parallel inductors for $t \ge 0$ is -30e ^{-5t} mV.				
	 (i) If the parallel inductors are replaced by a single induinductance? (ii) What is the initial current and its reference dirinductor? (iii) Use the equivalent inductor to find i(t). 	(i)	If the parallel inductors are replaced by a single inductor, what is its			
			inductance? (2	2 marks)		
		(ii)	What is the initial current and its reference direction in the equivinductor?	uivalent		
			3 marks)			
		(iii)	Use the equivalent inductor to find i(t). (2	2 marks)		
		(iii) Fi K	Find $i_1(t)$ and $i_2(t)$. Verify that the solutions for $i_1(t)$, $i_2(t)$ and $i(t)$ sate Kirchhoff's current law	fy		
			(5 marks)		
(b)		An uncharged 0.2 μ F capacitor is driven by a triangular current pulse. The current pulse is described below. Derive the expression for the capacitor voltage and power for each of the four intervals needed to describe the current.				

$$i(t) = \begin{cases} 0, & t \le 0; \\ 5000t A, & 0 \le t \le 20 \ \mu s; \\ 0.2 - 5000t A, & 20 \le t \le 40 \ \mu s; \\ 0, & t \ge 40 \ \mu s. \end{cases}$$

BEE1113 / BEX10103

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Q6	(a)	For the network in Figure Q6(a), the initial current is 0.1 mA and the switch is closed at $t = 0$.		
		(i)	Find the expression of inductor current at $t > 0$. (3 mark	cs)
		(ii)	Plot the graph of inductor currents based on the expression obtained in part $Q6(a)(i)$.	
			(2 mark	cs)
		(iii)	Find current i_R for $t > 0$. (1 mar	k)
		(iv)	Describe what will happen to current i_R at $t > 0$ if the value of R_2 is increased (2 mark)	d. (s)
	(b) The switch in the circuit shown in Figure Q6(b) has been in position x for time. At $t = 0$, the switch is moved to position y.		witch in the circuit shown in Figure Q6(b) has been in position x for a long At $t = 0$, the switch is moved to position y .	
		(i)	Calculate the initial value of V_c . Hence, determine the final value of V_c . (2 mark	cs)
		(ii)	Find the time constant, τ of the circuit when the switch is in position y. (1 mar	:k)
		(iii)	State the expression for $V_c(t)$ when $t \ge 0$ and for $i(t) \ge 0^+$? (3 mark	(s)
	 (iv) Determine how long after the switch is in position y voltage equal zero. (v) Plot V_c and i versus t. 	Determine how long after the switch is in position y does the capacitor voltage equal zero.		
		(2 mark	(S)	
		(v)	Plot V_c and i versus t. (4 mark	cs)
Q7	(a)	Find voltag respo cause	e roots of the characteristic equation that overns the transient behavior of the shown in Figure Q7(a) if $R = 200 \Omega$, $L = 50 \text{ mH}$ and $C = 0.2 \mu\text{F}$. Will the e be overdamped, underdamped or critically damped? What value of R he response to be critically damped?	
			(10 mar)	(S)
	(b)	The s At t =	switch in the circuit of Figure Q7(b) has been at position A for a long time. = 0, the switch moved to position B. Determine	
		(i)	the value of $V_{\rm C}(t)$ for $t < 0$	

the expression for $V_C(t)$ for t > 0(ii)

(8 marks)

(2 marks)



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6

FINAL EXAMINATION

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