

UNIVERSITI TUN HUSSEIN ONN MALAYSIA

FINAL EXAMINATION SEMESTER II SESSION 2008/2009

SUBJECT NAME

: ELECTRICAL TECHNOLOGY

SUBJECT CODE

: BEE 1223

COURSE

: I BEE

EXAMINATION DATE

: APRIL/MAY 2009

DURATION

: 2 1/2 HOURS

INSTRUCTIONS

: ANSWER ANY FOUR (4)

QUESTIONS FROM THE SEVEN

(7) QUESTIONS GIVEN

THIS QUESTION PAPER CONSISTS OF SIX (6) PAGES

Q1 (a) A current source in a linear circuit has $i_r = 5$	os (2000) - 25°) A .
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- (i) Find the amplitude of the current
- (ii) Find the period of the current
- (iii) Find the frequency of the current
- (iv) Calculate i_s at t = 3.0 ms

(10 marks)

(b) Convert $v_L = 100 \sin(2000t + 180^\circ)$ to cosine form.

(3 marks)

- (c) Given the complex numbers $z_1 = -3 + j4$ and $z_2 = 12 + j5$, find:
 - (i) z_1z_2
 - (ii) $\frac{z_1}{z_2^*}$
 - (iii) $\frac{z_1+z_2}{z_1-z_2}$
 - (iv) $3z_1^*z_2$

(12 marks)

Q2 (a) Explain briefly the time-domain and frequency-domain paradigms with aid of appropriate diagrams and equations. Is it possible to combine the timedomain and the frequency-domain paradigms in one axis for analysis purposes?. Give support of your answer.

(9 marks)

(b) The current in a series circuit of $R = 5 \Omega$ and L = 30 mH lags the applied voltage by 80° . By drawing the impedance diagram, determine the source frequency and the impedance of this series circuit.

(8 marks)

(c) A series RC circuit with $R=27.5~\Omega$ and $C=66.7~\mu\text{F}$, has sinusoidal voltage and current, with angular frequency 1500 rad/s. Find the phase angle between the current and voltage.

(8 marks)

Q3 (a) Compute the equivalent impedance Z_{eq} and admittance Y_{eq} for the fourbranch circuit as shown in Figure Q3(a).

(9 marks)

(b) The total current (I_1) entering the circuit as shown in Figure Q3(a) is $33.0 \angle -13.0^{\circ} A$. Obtain the branch current I_2 and the voltage V_3 .

(8 marks)

- (c) Determine the current I_1 and I_2 in the parallel circuit of Figure Q3(c). (8 marks)
- Q4 (a) Briefly describe the node voltage method for a frequency domain network.

 (9 marks)
 - (b) Using the node voltage method as in part (a) above, obtain the current I in the network of Figure Q4 (b).

(16 marks)

(a) Show that the line-to-line voltage V_{Line} in a three phase system is $\sqrt{3}$ times the line-to-neutral voltage V_{Phase} .

(7 marks)

(b) A three-phase, pps system, with a rms line voltage 70.7 V, has a balanced delta connected load with impedances 20∠45°Ω. Obtain the line currents and draw the voltage-current phasor diagram.

(9 marks)

(c) A three-phase, three wire nps system, with a rms line voltage of 106.1V, has a balanced Y- connected load with impedances $5\angle -30^{\circ}\Omega$. Obtain the currents and draw the voltage-current phasor diagram.

(9 marks)

Q6 (a) Explain briefly the following terms:

Use an appropriate diagram or equations to support your explanations.

- (i) Mutual inductance
- (ii) Ideal transformer
- (iii) Real transformer

(13 marks)

(b) A 150 kVA, 2400/240 V transformer has the following parameters:

 $R_p = 0.2 \Omega$

 $X_n = 0.45 \Omega$

 $R_c = 10000 \Omega$

 $R_s = 0.002 \ \Omega$

 $X_s = 0.0045 \ \Omega$

 $X_m = 1550 \Omega$

By using equivalent circuit referred to the primary:

- (i) Calculate the primary voltage of the transformer at rated load with 0.8 lagging power factor
- (ii) Determine the efficiency and voltage regulation of the transformer

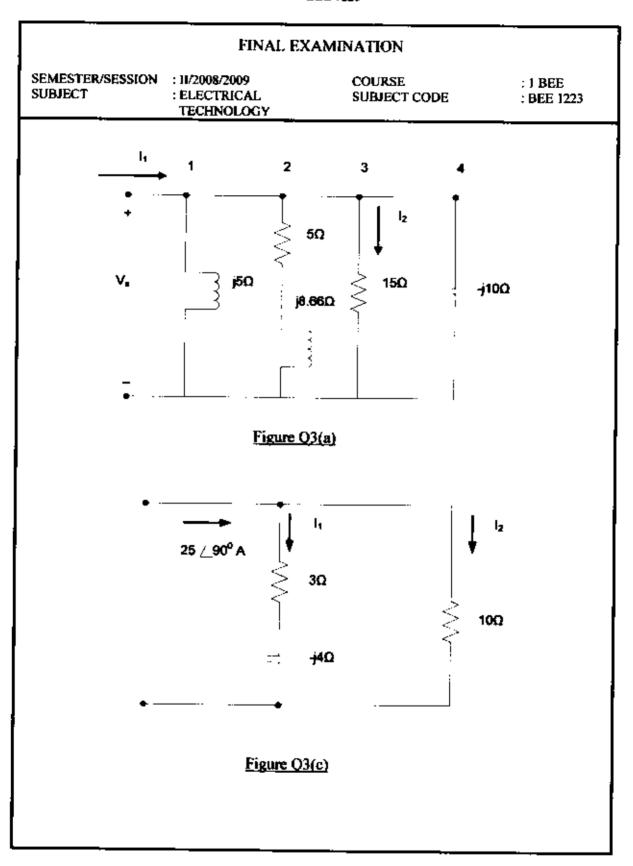
(12 marks)

- Q7 (a) Describe the characteristics of the following motors using an appropriate diagram and equations to support your explanations:
 - (i) DC shunt motor
 - (ii) DC series motor

(13 marks)

(b) A 250 V shunt generator has an armature resistance of 0.25 Ω and a field resistance of 125 Ω. At no load, the generator takes a line current of 5.0 A, while running at 1200 rpm. If the line current at full load is 52 A, calculate the full load speed.

(12 marks)



FINAL EXAMINATION

SEMESTER/SESSION : 11/2008/2009

COURSE

: 1 BEE

SUBJECT

: ELECTRICAL TECHNOLOGY SUBJECT CODE

: BEE 1223

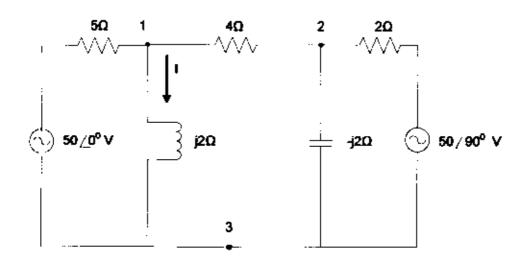


Figure Q4(b)

TABLE OF SPECIFICATION FOR FINAL EXAMINATION QUESTION AND MAPPING OF SLO TO QUESTIONS

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Code	BEE 1223	Course: ELECTRICAL TECHNOLOGY
Semester	: N	Session 2008/2009

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INSTRUCTIONS

- 1. Get the softcopy of this template Table Of Specification from Puan Norida Kasim, through email.
- 2. Adjust the template accordingly suitable for the number of question and no. of SLO assessed.
- 3. Only consider PLO1, PLO2, PLO4 and PLO5, which is PLO concerns with Technical Competency
- To simplify the overall assessment, each question assesses one PLO. But each PLO may be assessed by two Questions
- 6. Fill in the box within the SLO assessed, giving percentage marks for the related cognitive level
- 6. Calculate the total % marks for each cognitive level for each SLO.
- Tabulate the grand total of each cognitive level.

Cognitive Level	
1	D
2	36
3	50
4	14
6	0
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	100